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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/675,244	09/29/2003	Christian Muller	59992 (45107)	3489
21874	7590 11/29/2004		EXAM	INER
EDWARDS	& ANGELL, LLP		NGUYE	N, HIEP
P.O. BOX 55874 BOSTON, MA 02205			ART UNIT	PAPER NUMBER
BOSTON, M.	A 02203		2816	
			DATE MAILED: 11/29/2004	4

Please find below and/or attached an Office communication concerning this application or proceeding.

-		Application No.	Applicant(s)	
		10/675,244	MULLER ET AL.	
Office Action Summary		Examiner	Art Unit	
		Hiep Nguyen	2816	p
Period f	The MAILING DATE of this communication app or Reply	pears on the cover sheet with the	correspondence addr	ess
THE - Extraorder - If th - If N - Fail	HORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. ensions of time may be available under the provisions of 37 CFR 1.1 or SIX (6) MONTHS from the mailing date of this communication. en period for reply specified above is less than thirty (30) days, a reply operiod for reply is specified above, the maximum statutory period we ure to reply within the set or extended period for reply will, by statute or reply received by the Office later than three months after the mailing ned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tir y within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from t, cause the application to become ABANDONE	mely filed ys will be considered timely. the mailing date of this coming the mailing date of this coming the	munication.
Status				
1)⊠	Responsive to communication(s) filed on 30 A	<u>ugust 2004</u> .		
2a)⊠	This action is FINAL . 2b) ☐ This	action is non-final.		
3)[Since this application is in condition for alloward closed in accordance with the practice under E	•		nerits is
Disposit	tion of Claims			
5)⊠ 6)⊠ 7)⊠	Claim(s) <u>14-24</u> is/are pending in the application 4a) Of the above claim(s) is/are withdray Claim(s) <u>20</u> is/are allowed. Claim(s) <u>24</u> is/are rejected. Claim(s) <u>14-19 and 21-23</u> is/are objected to. Claim(s) are subject to restriction and/or	wn from consideration.		
Applicat	tion Papers	•		
9)[The specification is objected to by the Examine	er.		
10)	The drawing(s) filed on is/are: a) acce	epted or b) objected to by the	Examiner.	
	Applicant may not request that any objection to the	drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).	
	Replacement drawing sheet(s) including the correct			
11)	The oath or declaration is objected to by the Ex	caminer. Note the attached Office	Action or form PTO	-152.
Priority	under 35 U.S.C. § 119			
	Acknowledgment is made of a claim for foreign	s have been received. s have been received in Applicati rity documents have been receive	on No	age
* (See the attached detailed Office action for a list	of the certified copies not receive	ed.	
Attachmer	nt(s)			
_	ce of References Cited (PTO-892)	4) Interview Summary		
3) 🔲 Infor	ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) er No(s)/Mail Date	Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:		52)

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DETAILED ACTION

Response to Amendment

The amendment filed on 08-30-04 has been received and entered in the case. New ground of rejections necessitated by the amendment is set forth below.

Claim Objections

Claims 14 and 19 are objected to because of the following informalities: letter "if" is not a positive recitation. It should be changed to --when--; the recitation "the path" in claims 17 and 18 lacks antecedent basis.

Appropriate correction is required.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 24 is rejected under 35, U.S.C. 102(b) as being anticipated by Hung et al. (6,018,257).

Regarding claim 24, figures 2, 7 and 9 of Hung show a driver circuit comprising: a circuit node (OUTPUT),

at least two first transistors (102, 202), the load sections of which are connected in series and connect the circuit node with a predetermined voltage,

at least two second transistors (104, 204), the load sections of which are connected in series and connect the circuit node with a reference potential,

a control circuit (208) designed to regulate at least a first control voltage (210) on at least one transistor of the at least two first transistors and at least a second control voltage (214) on at least one transistor of the at least two second transistors dependent on a voltage at the circuit node (col. 4, lines 41-67),

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wherein said driver circuit is implemented in CMOS technology and the transistors are MOSFET'S, and

wherein a well control is provided for controlling well voltages (5V) of the at least two first transistors.

Allowable Subject Matter

Claim 20 is allowed.

Claims 14-19 and 21-23 would be allowable if rewritten or amended to overcome the objections set forth in this Office action.

Claims 14-18 and 21-23 are objected to because the prior art (US pat. 6,018,257) fails to teach or fairly suggest a driver comprising a control circuit which is designed to increase the control voltages in an inactive state of the driver circuit, when at least one threshold value is exceeded by the voltage on the circuit node, and wherein the control circuit comprises an electrical path from the circuit node to the reference potential, which electrical path has at least one diode to pre-set the at least one threshold value as called for in claim 14.

Claim 19 is objected to because the because the prior art (US pat. 6,018,257) fails to teach or fairly suggest a driver comprising a control circuit having active and inactive state; the control circuit increasing the control voltages in the inactive state when the voltage at the circuit node exceeds a threshold value; the control having an electrical path comprising transistor and diode for regulating the control voltage when the voltage at the circuit node is below a threshold voltage.

Claim 20 is allowed because the prior art (US pat. 6,018,257) fails to teach or fairly suggest a driver comprising a control circuit which is designed to change the driver circuit dependent on an enabling signal into an active or inactive state, and wherein the driver circuit is configured in such a manner that in the active state it consumes no static power.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hiep Nguyen whose telephone number is (571) 272-1752. The examiner can normally be reached on Monday to Friday from 7:30am to 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hiep Nguyen

11-15-04

TUANT. LAM
PRIMARY EXAMINER